Task No 01:

Verify the functionality of NOT, AND, OR, XOR, NAND, NOR, and XNOR Gates using MultiSim by designing the gates and creating their associated Truth Tables. Attach circuit snapshots and Truth Table illustrations for verification.

Solution:

Output:

Task No 02:

Implement the following equations on MultiSim and verify through Truth Table. (Attach the circuit snapshots)

1. (A + B) . (A . C)
2. A + B + B.C .(B+C)
3. AB’CD + A (B + C) + BC’D + ABD + BC’
4. (A + B’ + C ) . (A’ + C’ + D) . (B + C + D’)
5. (A’) . (A + B) + (B + A) . (A + B’)

Output: